IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Allen, et al.

Serial No.: 10/709,292

Filed: April 27, 2004

Group Art Unit: 2825

Examiner: Unknown

For: INTEGRATED CIRCUIT YIELD ENHANCEMENT USING VORONOI

DIAGRAMS

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Frederick W. Gibb, III

Commissioner of Patents PO BOX 1450 Alexandria, VA 22313-1450 RECEIVED CENTRAL FAX CENTER

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PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits and calculation of the filing fee, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please amend the specification in the application as follows: